

L Number	Hits	Search Text	DB	Time stamp
-	6	yield same virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/04 13:44
-	12	lock same virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/04 13:28
-	4	relinquish same virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/04 13:46
-	3	cooperat\$ same virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/04 13:48
-	2	deadlock same virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/04 13:49
-	1	hypervisor and virtual adj machine and "spin-lock"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/04 13:50
-	5	virtual adj machine and "spin-lock"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/04 13:50
-	3	hypervisor and virtual adj machine and "target processor"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/04 13:53
-	27	virtual adj machine same (target adj processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/04 13:54
-	9	virtual adj machine same (target adj processor) not (compile compiling compiled)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/04 13:54
-	60	(718.clas.) and virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/04 13:57
-	8	(718/106.ccls.) and virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/04 14:19
-	16	(718/107.ccls. 718/108.ccls.) and virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/04 14:24
-	2	5325526.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/04 14:56

-	5	lpar and schedul\$ and yield	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 14:27
-	5	virtual adj machine and request\$ adj yield	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 14:33
-	15	virtual adj machine and cooperat\$ adj2 schedul\$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 14:40
-	10	virtual adj processor and yield and (reassign repartition dispatch reallocate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 14:51
-	9	virtual adj processor and predicated	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 14:54
-	18	virtual adj processor and (sacrifice donate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 14:59
-	11	mach and virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 15:31
-	5	virtual adj processor and (hand adj off)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 15:31



High Performance Computing Group



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Overview

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1994 HPC Group Publications

Topics

PhD Thesis

Publications

- Marisa Gil. *Cooperación entre la aplicación y el kernel para la planificación de flujos, en sistemas multiprocesadores, como soporte al paralelismo.* PhD thesis, Universitat Politècnica de Catalunya (UPC), July 1994. Advisor José I. Navarro.

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- Dolors Royo and Miguel Valero-García. *Benchmarking: una técnica para la evaluación rápida de computadores.* *Informática y Automática*, pp. 19-27, vol. 27, no. 1, March 1994.
- Mateo Valero, Eduard Ayguadé and Montse Peiron. *Network synchronization and Out-of-Order Access to Vectors.* *Parallel Processing Letters*, pp. 405-415, vol. 4, no. 4, December 1994.
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